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FACSIMILE COVER SHEET

Examiner: Office of Initial Patent Examination
Customer Service Center

Date: July 7, 2004

Client Code: 0717

Facsimile No.: 703-746-9195

From: N. Scott Pierce, Esq.

Subject: Request for Corrected Filing Receipt

Docket No.: 0717.2013-013

Applicants: Roger E. Welser, Paul M. Deluca and Noren Pan

Application No.: 10/824/697

Filing Date: April 14, 2004

Number of pages including this cover sheet: 7

Please confirm receipt of facsimile: Yes X No

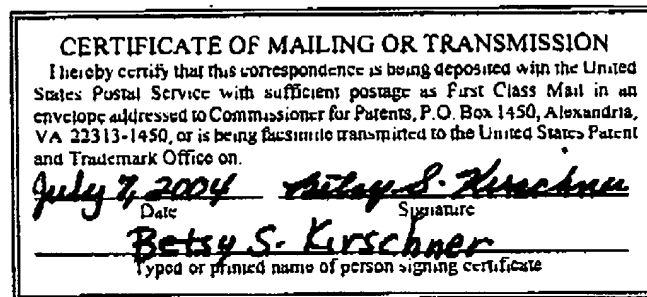
Sir:

Please see attached.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Roger E. Welser, Paul M. Deluca and Noren Pan
Application No.: 10/824,697 Group: 2813
Filed: April 14, 2004 Examiner: Not Assigned
Confirmation No.: 6799
For: BIPOLAR TRANSISTOR WITH LATTICE MATCHED BASE
LAYER



REQUEST FOR CORRECTED FILING RECEIPT
FOR UTILITY APPLICATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

We hereby request that the following items of information be corrected in the Filing Receipt dated June 28, 2004, for the subject application, and received in this office on June 30, 2004.

Please replace "Rojer E. Welser" with "Roger E. Welser"

10/824,697

-2-

Enclosed are copies of the Filing Receipt with changes noted in red and a copy of the Declaration for Patent Application,.

Pursuant to instructions in the February 29, 2000 O.G., we hereby request that the errors which are identified above be corrected in the captioned application to which this request for correction is directed. It is understood that the Patent Office will issue an automatically-generated, corrected Filing Receipt in this and, if applicable, any other affected applications.

Respectfully submitted,

HAMILTON, BROOK, SMITH & REYNOLDS, P.C.

By 

N. Scott Pierce

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Date: 7/6/04



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/824,697	04/14/2004	2813	385	0717.2013-013	7	17	1

CONFIRMATION NO. 6799

021005

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FILING RECEIPT



OC000000013080798

Date Mailed: 06/28/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s) **"ROGER"**

~~Roger E. Welser~~, Providence, RI;
 Paul M. Deluca, Providence, RI;
 Noren Pan, Wilmette, IL.

Assignment For Published Patent Application

Kopin Corporation, Taunton, MA;

Domestic Priority data as claimed by applicant

This application is a DIV of 09/995,079 11/27/2001 PAT 6,750,480
 which claims benefit of 60/253,159 11/27/2000

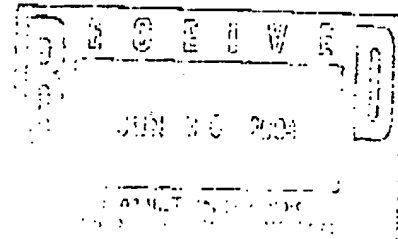
Foreign Applications

Projected Publication Date: To Be Determined - pending completion of Security Review

Non-Publication Request: No

Early Publication Request: No

** SMALL ENTITY **



Title

Bipolar transistor with lattice matched base layer

Preliminary Class

257

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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